

GOVERNMENT OF INDIA
MINISTRY OF ELECTRONICS AND INFORMATION TECHNOLOGY
LOK SABHA
UNSTARRED QUESTION NO. 575
TO BE ANSWERED ON: 23.07.2025

SEMICONDUCTOR FABRICATION UNITS

575. SHRI ARVIND DHARMAPURI:

Will the Minister of ELECTRONICS AND INFORMATION TECHNOLOGY be pleased to state:

- (a) the number of proposals received and approved under ISM (Indian Semiconductor Mission) for semiconductor fabrication units till 31 March, 2025;
- (b) the details of infrastructure readiness status for these fabrication units, region-wise;
- (c) the expected timeline for commercial production from the approved units;
- (d) whether the Government proposes to establish semiconductor fabrication units in Telangana and if so, the details thereof; and
- (e) whether any foreign players have entered joint ventures under the Design Linked Incentive (DLI) and if so, the details thereof, State/UT-wise?

ANSWER

MINISTER OF STATE FOR ELECTRONICS AND INFORMATION TECHNOLOGY
(SHRI JITIN PRASADA)

- (a) _to (e): Government introduced Semicon India Programme with an outlay of Rs 76,000 crore, which provides:
- i. Fiscal support of 50% of the project cost on *pari-passu* basis for setting up of Silicon Complementary Metal-Oxide-Semiconductor (CMOS) based Semiconductor Fabs in India.
 - ii. Fiscal support of 50% of Project Cost on *pari-passu* basis for setting up of Display Fabs in India.
 - iii. Fiscal support of 50% of the Capital Expenditure on *pari-passu* basis for setting up of Compound Semiconductors / Silicon Photonics (SiPh) / Sensors (including Micro-Electro-Mechanical Systems) Fab/ Discrete Semiconductor Fab and Semiconductor Assembly, Testing, Marking and Packaging (ATMP) / Outsourced Semiconductor Assembly and Test (OSAT) facilities in India.
 - iv. Product Design Linked Incentive of up to 50% of the eligible expenditure subject to a ceiling of ₹15 Crore per application and also “Deployment Linked Incentive” of 6% to 4% of net sales turnover over 5 years subject to a ceiling of ₹30 Crore per application for incentivising chip design.

Under Semicon India Programme, Government has approved 6 semiconductor manufacturing projects with cumulative investment of around Rs. 1,55,000 crore.

It is a pan India programme and the applicants are free to choose locations for their projects. Many states have designed their semiconductor policies to align with the Semicon India Programme. Applications are evaluated on technical and financial aspects. Applicants choose the project location depending on various parameters such as uninterrupted power supply, availability and quality of water, land availability, proximity of airport, incentives offered under State government policies and other measures relating to ease of doing business provided by the State Governments.

Under the Design Linked Incentive (DLI) scheme, the fiscal support is provided to the approved startup and MSMEs. Out of 22 design companies approved under the scheme so far, 3 companies are in the State of Telangana. Apart from the above, design infrastructure support has also been approved to 11 companies in Telangana. Moreover, under Chips to start-up (C2S) programme 22 institutes from Telangana are being supported with design tools and 6 institutes are being provided financial support. The details of beneficiaries in the state of Telangana are Annexed.

List of beneficiary companies for financial support under DLI in the State of Telangana:

1. Green PMU Semi Pvt Ltd
2. WiSig Networks Pvt Ltd
3. MosChip Technologies Pvt Ltd

List of beneficiary companies for design tool support under DLI in the State of Telangana:

1. Green PMU Semi Pvt. Ltd.
2. Wisig Networks Pvt. Ltd.
3. Dheyo AI Pvt. Ltd.
4. AMPICQ Pvt. Ltd.
5. BITSILICA Pvt Ltd
6. Manjeera Digital Systems Pvt. Ltd.
7. Spintronics AI Semiconductors Pvt. Ltd.
8. SmartKosh Technologies Pvt. Ltd.
9. Vconnectech Systems Pvt. Ltd.
10. Speedbus Semiconductors Pvt. Ltd.
11. WnPSemicon LLP

List of beneficiary academic institutes for financial support under C2S Programme in the State of Telangana:

1. University of Hyderabad
2. International Institute of Information Technology Hyderabad
3. Osmania University College of Engineering
4. Chaitanya Bharathi Institute of Technology, Hyderabad
5. Indian Institute of Technology Hyderabad
6. National Institute of Technology Warangal

List of beneficiary academic institutes for design tool support under C2S Programme in the State of Telangana:

1. University of Hyderabad
2. International Institute of Information Technology Hyderabad
3. Osmania University College of Engineering
4. Chaitanya Bharathi Institute of Technology, Hyderabad
5. Indian Institute of Technology Hyderabad
6. National Institute of Technology Warangal
7. JNTUH University College of Engineering Hyderabad
8. Birla Institute of Technology & Science (BITS), Hyderabad
9. SR University, Warangal
10. ACTS, CDAC, Hyderabad
11. VNRVJIET, Hyderabad
12. MJ College of Engineering and Technology (MJCET), Hyderabad
13. Geethanjali College of Engineering and Technology, Telangana
14. B V Raju Institute of Technology, Narsapur, Telangana
15. GNITS, Hyderabad
16. MLR Institute of Technology, Dundigal
17. Vasavi College of Engineering, Hyderabad
18. ICFAI Foundation for Higher Education, Hyderabad
19. T K R College of Engineering & Technology, Hyderabad

20. CVR College of Engineering, Telangana
21. CMR Engineering College, Hyderabad
22. Vardhaman College of Engineering, Hyderabad
