GOVERNMENT OF INDIA
MINISTRY OF ELECTRONICS AND INFORMATION TECHNOLOGY
LOK SABHA
UNSTARRED QUESTION NO. 5093
TO BE ANSWERED ON: 02.04.2025

SEMICONDUCTOR MANUFACTURING UNITS

5093. SMT. SUPRIYA SULE:
SHRI BAJRANG MANOHAR SONWANE:
SHRI NILESH DNYANDEV LANKE:
DR. AMOL RAMSING KOLHE:
SHRI MOHITE PATIL DHAIRYASHEEL RAJSINH:
PROF. VARSHA EKNATH GAIKWAD:
SHRI BHASKAR MURLIDHAR BHAGARE:
SHRI SANJAY DINA PATIL:

Will the Minister of ELECTRONICS AND INFORMATION TECHNOLOGY be pleased to state:

(a) the total number of semiconductor manufacturing units set up in Maharashtra during the last three years, location-wise;
(b) whether demand for semiconductors has increased in the country particularly in the automobile, electronics and consumer industries and if so, the details thereof;
(c) the total number of Semiconductor Fabrication Units (fabs) and Semiconductor Assembly, Testing, Marking and Packaging (ATMP) units set up in the country including Maharashtra during each of the last three years, location-wise;
(d) the key initiatives being undertaken by the Government to attract foreign investment, strengthen research & development and promote domestic chip manufacturing to achieve self-reliance in semiconductors;
(e) whether the Government has entered into any international collaborations or partnerships for semiconductor manufacturing and technology transfer and if so, the details thereof; and
(f) the other measures being taken by the Government to make India self-reliant in semiconductor manufacturing including the role of Maharashtra in this initiative?

ANSWER

MINISTER OF STATE FOR ELECTRONICS AND INFORMATION TECHNOLOGY
(SHRI JITIN PRASADA)

(a), (c), (d) and (e):Government has approved Semicon India programme with a total outlay of Rs 76,000 crore for the development of semiconductor and display manufacturing ecosystem in India. which provides:

(i) Fiscal support of 50% of the project cost on pari-passu basis for setting up of Silicon Complementary Metal-Oxide-Semiconductor (CMOS) based Semiconductor Fabs in India.
(ii) Fiscal support of 50% of Project Cost on pari-passu basis for setting up of Display Fabs in India.
(iii) Fiscal support of 50% of the Capital Expenditure on pari-passu basis for setting up of Compound Semiconductors / Silicon Photonics (SiPh) / Sensors (including Micro-Electro-Mechanical Systems) Fab/ Discrete Semiconductor Fab and Semiconductor Assembly, Testing, Marking and Packaging (ATMP) / Outsourced Semiconductor Assembly and Test (OSAT) facilities in India.

(iv) Product Design Linked Incentive of up to 50% of the eligible expenditure subject to a ceiling of ₹15 Crore per application and also "Deployment Linked Incentive" of 6% to 4% of net sales turnover over 5 years subject to a ceiling of ₹30 Crore per application for incentivising chip design.

Government has also approved modernisation of Semi-Conductor Laboratory, Mohali to enhance efficiency and cycle time.

To strengthen semiconductor manufacturing and creating a semiconductor ecosystem in the country, Government has entered in Memorandum of Understanding (MoU) with USA, European Union, Japan and Singapore.

This Programme is a pan India programme. Under this programme, Government has approved five (5) semiconductor manufacturing projects that includes One Semiconductor Fabrication facility and Four (4) Semiconductor ATMP/OSAT facilities under Semicon India Program with cumulative investment of around Rs. 1,52,000 crore.

17 semiconductor design companies have also been approved under the Design Linked Incentive (DLI) Scheme to design chips for Indian products. Further 67 companies (including 4 companies from Maharashtra) have been approved under DLI Scheme for EDA tool infrastructure support.

ISM has 19 approved beneficiaries in the state of Maharashtra as detailed in the **Annexure**.

(b):Semiconductors are central to the global digital economy. As per industry estimates, the global semiconductor demand is estimated to reach USD 1 trillion by 2030. Industry estimates indicate, the semiconductor demand in India to reach around USD 100 billion by 2030,with the automobile, consumer electronics, and industrial sectors being the major contributors to this demand.

(f):The other steps taken by the Government to make India self-reliant in semiconductor manufacturing sector:

i. **Talent Development:** Recognizing the importance of Talent development in Semiconductor domain, Government of India has been making significant efforts to develop India as a global talent hub for semiconductors. New curriculums for Diploma, Under-Graduate, Post Graduate and Doctoral programmes have been prepared in consultation with Industry which are being implemented by Ministry of Education and AICTE.
ii. **Chips to Start-up (C2S) Programme:** has also been initiated with an aim to train 85,000 number of industry-ready manpower specialized in the area of VLSI/ Chip design/ Embedded System Design and leapfrog in ESDM space by way of inculcating the culture of Chip/ System-on-Chip (SoC)/ System Level Design and act as catalyst for growth of Start-ups involved in semiconductor design in the country.
iii. **Collaborationswith other nations:** Government of India hasalso established collaborative partnership with relevant stakeholders such as MoU with USA, Japan, European Union. Additionally, India Semiconductor Mission has signed MoU with Purdue University and IBM India Private Limited.

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Annexure

List of Beneficiaries for EDA tool support under Design Linked Incentive Scheme of Semicon India Programme in the state of Maharashtra

1 Panache Digilife Ltd
2 Vasbeam Pvt Ltd
3 Tahinium Technologies Pvt Limited
4 Imumbai Semiconductors Private Limited

List of Beneficiaries under Chips -to- Start (C2S) Program in the state of Maharashtra

1 Indian Institute of Technology Bombay
2 V. E. S. Institute of Technology, Mumbai
3 National Institute of Electronics & Information Technology Aurangabad
4 Cummins College of Engineering for Women, Pune
5 Indian Institute of Information Technology Pune
6 M/s Aryabhata Circuits and Research (ABCR) Labs Pvt. Ltd
7 M/s Panache Digilife Limited
8 VNIT Nagpur
9 COEP Technological University, Pune
10 Symbiosis Institute of Technology, Pune
11 SVERI's College of Engineering, Pandharpur
12 Shri Sant Gajanan Maharaj College of Engineering (SSGMCE), Shegaon, Maharashtra
13 ACTS, CDAC, Pune
14 K J Somaiya College of Engineering, Mumbai
15 Vishwakarma Institute of Technology, Pune
16 Pimpri Chinchwad College of Engineering, Pune
17 Ramdeobaba University, Nagpur
18 Yeshwantrao Chavan College of Engineering,
19 Nagpur Defence Institute of Advanced Technology, Pune

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