

GOVERNMENT OF INDIA
MINISTRY OF ELECTRONICS AND INFORMATION TECHNOLOGY
LOK SABHA
UNSTARRED QUESTION NO. 2072
TO BE ANSWERED ON: 12.03.2025

SKILL DEVELOPMENT IN SEMICONDUCTOR SECTOR

2072. SHRI DHAIRYASHEEL SAMBAJIRAO MANE:
SHRI CHAVAN RAVINDRA VASANTRAO:
SHRI SUDHEER GUPTA:

Will the Minister of ELECTRONICS AND INFORMATION TECHNOLOGY be pleased to state:

- (a) whether the Government proposes to facilitate training activities including skill development in the country's semiconductor sector under India Semiconductor Mission;
- (b) if so, the details thereof and the steps taken in this regard;
- (c) whether the Government has collaborated with institutions like Indian Institutes of Technology (IITs) and private colleges to train and skill the individuals in the said sector and if so, the details thereof;
- (d) whether the Government proposes to skill the people by providing training to them in overseas where such facilities are available; and
- (e) if so, the details thereof and the expenditure likely to be incurred thereon?

ANSWER

MINISTER OF STATE FOR ELECTRONICS AND INFORMATION TECHNOLOGY
(SHRI JITIN PRASADA)

(a) to (e): Government has approved Semicon India programme with a total outlay of Rs 76,000 crore for the development of semiconductor and display manufacturing ecosystem in the country. Semiconductor manufacturing is very complex and Technology intensive sector which requires specialized skilled manpower. To address this, the following measures have been taken by the Government:

1. All India Council for Technical Education (AICTE) has launched the new curriculum for B. Tech in Electronics Engineering (Very Large-Scale Integration (VLSI) Design and Technology), Diploma in Integrated Circuit (IC) manufacturing and Minor Degree in Electronics Engineering (VLSI Design and Technology), as a step towards creation of Talent pool in Semiconductor domain.
2. Government has launched the Chips to Startup ('C2S') programme which plans to train 85,000 industry ready workforce at about 113 participating institutions in VLSI and Embedded System Design. More than 43,000 engineering students have been onboarded for training at 113 organizations under C2S Programme till date.
3. A Skilled Manpower Advanced Research and Training (SMART) Lab has been setup in NIELIT Calicut in 2022 with an aim to train one lakh engineers nation-wide within 5 years in VLSI and Embedded System design. More than 42,000 engineers have been trained nationwide using the SMART Lab.
4. Further, the following collaborations/ partnerships have been entered into by India Semiconductor Mission (ISM) to encourage skill development:
 - (i) **MoU between ISM with IISc and Lam Research:** To train ~60,000 Indian engineers in the upcoming 10 years through Lam Research's Semiverse platform.

- (ii) **MoU between ISM and IBM:** To facilitate Indian students/professionals to build a broad skill base by gaining access to laboratories and research focal centers and establishing internship and fellowship programs.
- (iii) **MoU between ISM with Purdue University:** To promote the cutting-edge research and development and commercialization thereof, curating skilled talent pool and investment opportunities in India enabling the Indian professionals to explore their potential in the semiconductor and display space.
