

GOVERNMENT OF INDIA
MINISTRY OF ELECTRONICS AND INFORMATION TECHNOLOGY
LOK SABHA
STARRED QUESTION NO. *199
TO BE ANSWERED ON: 12.03.2025

SEMICONDUCTOR MISSION IN TELANGANA

***199. SHRI VAMSI KRISHNA GADDAM:**

Will the Minister of ELECTRONICS AND INFORMATION TECHNOLOGY be pleased to state:

- (a) the total number of semiconductors manufacturing units set up in the country during each of the last three years, location-wise;
- (b) whether the Union Government has considered the request of the Government of Telangana for the establishment of a Semiconductor Mission in Telangana, if so, details thereof and the action taken thereon;
- (c) the status of approvals for the three new projects including one FAB and two OSAT projects in Telangana under the India Semiconductor Mission (ISM);
- (d) the funds allocated and disbursed, if any, for the ISM till date, State-wise particularly Telangana; and
- (e) the reasons for the delay or denial of such projects in Telangana, despite its readiness and proactive communication, in case these are still pending?

ANSWER

MINISTER OF ELECTRONICS AND INFORMATION TECHNOLOGY
(SHRI ASHWINI VAISHNAW)

(a) to (e): A Statement is laid on the Table of the House.

STATEMENT REFERRED TO IN THE REPLY TO LOK SABHA STARRED QUESTION NO. *199 FOR 12.03.2025, REGARDING SEMICONDUCTOR MISSION IN TELANGANA.

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(a): Government has approved Semicon India programme with a total outlay of Rs 76,000 crore for the development of semiconductor and display manufacturing ecosystem in the country, which provides:

- i. Fiscal support of 50% of the project cost on *pari-passu* basis for setting up of Silicon Complementary Metal-Oxide-Semiconductor (CMOS) based Semiconductor Fabs in India.
- ii. Fiscal support of 50% of Project Cost on *pari-passu* basis for setting up of Display Fabs in India.
- iii. Fiscal support of 50% of the Capital Expenditure on *pari-passu* basis for setting up of Compound Semiconductors / Silicon Photonics (SiPh) / Sensors (including Micro-Electro-Mechanical Systems) Fab/ Discrete Semiconductor Fab and Semiconductor Assembly, Testing, Marking and Packaging (ATMP) / Outsourced Semiconductor Assembly and Test (OSAT) facilities in India.
- iv. Product Design Linked Incentive of up to 50% of the eligible expenditure subject to a ceiling of ₹15 Crore per application and also “Deployment Linked Incentive” of 6% to 4% of net sales turnover over 5 years subject to a ceiling of ₹30 Crore per application for incentivising chip design.

Government has also approved modernisation of Semi-Conductor Laboratory, Mohali to enhance efficiency and cycle time.

Under Semicon India Programme, during last three years, Government has approved five (5) semiconductor manufacturing projects with cumulative investment of around Rs. 1,52,000 crore. This includes four projects in the state of Gujarat and one in the state of Assam.

(b): India Semiconductor Mission supports setting up the semiconductor manufacturing units in the country. It does not have any provision for setting up of Semiconductor Missions in any State. States are free to setup their own semiconductor Missions.

(c): These proposals are currently under different stages of appraisal and approval process. Further, 3 proposals for fiscal support and 9 proposals for EDA tool access support have been approved under Design Linked Incentive (DLI) Scheme in the State of Telangana. Additionally, 7 institutes / universities have been approved under Chip to Startup (C2S) Programme in the state of Telangana. The list of beneficiaries under DLI and C2S Programme in the state of Telangana is at **Annexure**.

(d): Till date, Rs 60,269 crore has been committed for the 5 approved manufacturing projects.

(e): The approvals are done after examination by Techno-Financial Advisory Group (TFAG), which examines the proposals from Technical and financial aspects. Further, the processing also depends on the relevant document submissions by the applicants like Technology tie up agreement, Offtake agreements, documents related to financial closure etc.

List of Beneficiaries for financial support under Design Linked Incentive Scheme of Semicon India Programme in the state of Telangana

1. Green PMU Semi Pvt Ltd
2. WiSig Networks Pvt Ltd
3. MosChip Technologies Pvt Ltd

List of Beneficiaries for EDA tool support under Design Linked Incentive Scheme of Semicon India Programme in the state of Telangana

- 1 Green PMU Semi Pvt Ltd
- 2 WiSig Networks Pvt Ltd
- 3 Dheyo AI Private Limited
- 4 AMPICQ Private Limited
- 5 BITSILICA Pvt Ltd
- 6 Manjeera Digital Systems Pvt. Ltd.
- 7 Spintronics AI Semiconductors Private Limited
- 8 SmartKosh Technologies Pvt Ltd
- 9 Vconnectech Systems Private Limited

List of Beneficiaries under Chips -to- Start (C2S) Program in the state of Telangana

1. University of Hyderabad
2. International Institute of Information Technology Hyderabad
3. University College of Engineering Osmania University, Hyderabad
4. Chaitanya Bharathi Institute of Technology, Hyderabad
5. Indian Institute of Technology Hyderabad
6. National Institute of Technology Warangal
7. JNTUH University College of Engineering Hyderabad
