

GOVERNMENT OF INDIA
MINISTRY OF ELECTRONICS AND INFORMATION TECHNOLOGY
LOK SABHA
UNSTARRED QUESTION NO. 444
TO BE ANSWERED ON: 27.11.2024

**MODIFIED PROGRAMME FOR DEVELOPMENT OF SEMICONDUCTORS AND
DISPLAY MANUFACTURING ECOSYSTEM**

444. SHRI B K PARTHASARATHI:

Will the Minister of ELECTRONICS AND INFORMATION TECHNOLOGY be pleased to state:

- (a) the current status of approved semiconductor and display manufacturing units in India under the modified Programme for Development of Semiconductors and Display Manufacturing Ecosystem;
- (b) the funds allocated, disbursed and utilized for the development of these facilities under the aforementioned programme;
- (c) the details of proposals received from Andhra Pradesh to establish semiconductor manufacturing units/facilities during each of the last three years;
- (d) the details of approved projects from Andhra Pradesh under the programme including their status and the costs incurred thereon; and
- (e) the details of proposals received from the State of Andhra Pradesh during each of the last three years still pending for clearance?

ANSWER

MINISTER OF STATE FOR ELECTRONICS AND INFORMATION TECHNOLOGY
(SHRI JITIN PRASADA)

(a) to (e): Government has approved Semicon India programme with a total outlay of Rs 76,000 crore for the development of semiconductor and display manufacturing ecosystem in the country for:

- Setting up of Semiconductor Fabs in India which provides for a fiscal support of 50% of the project cost on *pari-passu* basis for setting up of Silicon CMOS based Semiconductor Fabs in India.
- Setting up of Display Fabs in India which provides for a fiscal support of 50% of Project Cost on *pari-passu* basis for setting up of Display Fabs in India.
- Setting up of Compound Semiconductors / Silicon Photonics / Sensors Fab / Discrete Semiconductors Fab and Semiconductor Assembly, Testing, Marking and Packaging (ATMP) / OSAT facilities in India which provides for a fiscal support of 50% of the Capital Expenditure on *pari-passu* basis for setting up of Compound Semiconductors / Silicon Photonics (SiPh) / Sensors (including MEMS) Fab/ Discrete Semiconductor Fab and Semiconductor ATMP / OSAT facilities in India.
- Providing incentives on design through 'Design Linked Incentive (DLI) Scheme' which provides "Product Design Linked Incentive" of up to 50% of the eligible expenditure subject to a ceiling of ₹15 Crore per application and also "Deployment Linked Incentive" of 6% to 4% of net sales turnover over 5 years subject to a ceiling of ₹30 Crore per application.
- Government has also approved modernisation of Semi-Conductor Laboratory, Mohali to enhance efficiency and cycle time.

Government has approved five (5) semiconductor projects with cumulative investment of Rs 1 lakh 52 thousand Crore under the Semicon India Programme. All these approved projects are in different phases of implementation. Further, 15 semiconductor design companies have also been approved under the Design Linked Incentive Scheme to design chips for Indian products. Additionally, 41 semiconductor design companies have been approved for access of the tools required for designing the chips (called EDA tools) which is being made available by National EDA Tool Grid setup at ChipIN Centre at C-DAC Bengaluru.

To create the skilled manpower for chip design, Government has launched the Chips to Startup ('C2S') programme which plans to train 85 thousand specialized workforce at about 113 participating institutions in VLSI and Embedded System Design. List of institutions in Andhra Pradesh supported under these initiatives is in Annexure-I.

Annexure-I

Institutions based in Andhra Pradesh supported under C2S

1. Indian Institute of Information Technology Design and Manufacturing Kurnool
2. Indian Institute of Technology Tirupati
3. JNTU, Anantapur
4. JNTU, Kakinada
5. National Institute of Technology Andhra Pradesh
6. Shri Vishnu Engineering College for Women, Bhimavaram
7. SRM University, AP
8. V.R. Siddhartha Engineering College, Vijayawada
9. Vignan's Foundation for Science Technology and Research, Guntur
10. VIT-AP University, Amaravati, AP
