GOVERNMENT OF INDIA
MINISTRY OF ELECTRONICS AND INFORMATION TECHNOLOGY
RAJYA SABHA
UNSTARRED QUESTION NO. 715
TO BE ANSWERED ON: 05.12.2025

INVESTMENT UNDER INDIA SEMICONDUCTOR MISSION

715. SHRI HARSH VARDHAN SHRINGLA:

Will the Minister of ELECTRONICS AND INFORMATION TECHNOLOGY be pleased to state:

(a) status of approved semiconductor fabrication units;
(b) total investment committed under the India Semiconductor Mission;
(c) expected employment generation;
(d) collaborations with global technology firms; and
(e) measures to develop a skilled semiconductor workforce?

ANSWER

MINISTER OF STATE FOR ELECTRONICS AND INFORMATION TECHNOLOGY
(SHRI JITIN PRASADA)

(a) to (d): The Government attaches a high priority for the semiconductor sector as it is a foundational industry for the economy and also plays a critical role in making India Atma Nirbhar in electronics manufacturing. Accordingly, Semicon India programme was launched by Government of India. Till date, Government has approved ten (10) semiconductor manufacturing projects under this programme.

The approved projects are under various phases of implementation. As semiconductor industry is a foundational and cross cutting industry, therefore these units are expected to have a cascading effect on employment generation in all sectors of the economy. As regards technology collaboration with global firms, many approved companies under Semicon India Programme have entered into technology partnership with global semiconductor companies from Taiwan, Japan, Thailand, UK and Malaysia etc.

(e): Government has adopted a comprehensive approach for skilling for semiconductor manufacturing including ATMP, fabrication and design. Following steps have been taken for development of skilled workforce in semiconductor sector:

(i) Chips to Startup (C2S) programme was launched with the objective to develop 85 thousand skilled manpower in semiconductor sector. Under this programme, engineering institutions are provided with necessary software and tools to design semiconductor chips. Approximately, 1 lakh students from 300 institutions have been enrolled and 255 training sessions on design flow have been conducted in partnership with leading companies under this programme.

(ii) Based on the recommendations of the 'Semicon India Future Skills Talent Committee' constituted by Government, All India Council for Technical Education (AICTE) launched following three courses:

(a) B. Tech in Electronics Engineering (VLSI Design)
(b) Diploma in Integrated Circuit (IC) manufacturing, and
(c) Minor Degree in Electronics Engineering (VLSI Design and Technology)

(iii) A Skilled Manpower Advanced Research and Training (SMART) Lab has been setup in NIELIT Calicut with an aim to train 1 lakh engineers nation-wide. More than 62 thousand engineers have already been trained.

(iv) ISM has also partnered with Lam Research for conducting a large-scale training programme in nanofabrication and process-engineering skills. These would further augment skilled workforce for ATMP and advanced packaging. The program aims to generate 60,000 trained manpower in next 10 years.

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