GOVERNMENT OF INDIA
MINISTRY OF ELECTRONICS AND INFORMATION TECHNOLOGY
RAJYA SABHA
UNSTARRED QUESTION NO. 711
TO BE ANSWERED ON: 05.12.2025

SEMICONDUCTOR MISSION AND MANUFACTURING

711. SHRI SUJEET KUMAR:
SMT. SANGEETA YADAV:
DR. SUMER SINGH SOLANKI:
DR. KAVITA PATIDAR:
SHRI MAYANKKUMAR NAYAK:
DR. ANIL SUKHDEORAO BONDE:
SHRI PRADIP KUMAR VARMA:
SHRI DEEPAK PRAKASH:
SHRI NARHARI AMIN:

Will the Minister of ELECTRONICS AND INFORMATION TECHNOLOGY be pleased to state:

(a) the current status of the first 28nm fab project and its expected date of commercial production;
(b) the manner in which the Ministry is expediting the clearance process for new proposals under the Semicon India Programme;
(c) whether Ministry has achieved the target for domestic chip design Startups under the Design Linked Incentive scheme;
(d) the manner in which the plans for developing the skilled workforce needed for the assembly, testing, and packaging; and
(e) the manner in which the Ministry is securing the supply of crucial chemicals and gases for the upcoming fabrication facilities?

ANSWER

MINISTER OF STATE FOR ELECTRONICS AND INFORMATION TECHNOLOGY
(SHRI JITIN PRASADA)

(a) to (e): The semiconductor development strategy is inspired by Hon'ble Prime Minister's vision of Atmanirbhar Bharat and Make in India, Make for the World. As a part of this strategy, India aims to develop a complete ecosystem ranging from design, fabrication, assembly, testing and packaging.

Given the foundational nature of semiconductor industry for the economy, the Government launched the 'Semicon India Programme' for the development of semiconductor and display manufacturing ecosystem in the country.

Government has approved ten (10) semiconductor manufacturing projects including the proposal of Tata Electronics Pvt. Ltd. for setting up a semiconductor fab facility in Dholera, Gujarat with an investment of Rs. 91,526 crore, which inter-alia includes 28nm Technology node.

The Government has instituted a streamlined mechanism through India Semiconductor Mission (ISM) for clearances of the proposals received under the Semicon India Programme. The applications are processed through the dedicated online portal.

Under Semicon India programme, Government is also providing incentives for chip design to start-ups & MSMEs through 'Design Linked Incentive (DLI) Scheme. Fiscal support has been approved for 24 applications under this scheme. Further, EDA tool support has been provided to 91 applicants enabling 47 lakh hours of design tool usage.

Development of Talent Pipeline:

Government has adopted a comprehensive approach for skilling for semiconductor manufacturing including ATMP, fabrication and design. Following steps have been taken for development of skilled workforce in semiconductor sector:

(i) **Chips to Startup (C2S)** programme was launched with the objective to develop 85 thousand skilled manpower in semiconductor sector. Under this programme, engineering institutions are provided with necessary software and tools to design semiconductor chips. Approximately, 1 lakh students from 300 institutions have been enrolled and 255 training sessions on design flow have been conducted in partnership with leading companies under this scheme.

(ii) Based on the recommendations of the 'Semicon India Future Skills Talent Committee' constituted by Government, All India Council for Technical Education (AICTE) launched following three courses:
   (a) B. Tech in Electronics Engineering (VLSI Design)
   (b) Diploma in Integrated Circuit (IC) manufacturing, and
   (c) Minor Degree in Electronics Engineering (VLSI Design and Technology)

(iii) A Skilled Manpower Advanced Research and Training (SMART) Lab has been setup in NIELIT Calicut with an aim to train 1 lakh engineers nation-wide. More than 62 thousand engineers have already been trained.

(iv) ISM has also partnered with Lam Research for conducting a large-scale training programme in nanofabrication and process-engineering skills. These would further augment skilled workforce for ATMP and advanced packaging. The program aims to generate 60,000 trained manpower in next 10 years.

Further, Government is also providing enabling platform to facilitate the partnership between Indian academia and approved beneficiaries under ISM. Government also enables such partnerships with global academia & training institutions through various MoUs signed with other countries.

The approved applicants tie up with various global and local supply chain partners to ensure long term supply of the chemicals, gases and other raw materials. Further, Government has also signed MoUs with the USA, EU, Japan, Singapore and the Netherlands which provides an enabling platform for B2B collaborations for securing supply chain partnerships of such approved companies.

******