

# O.I.H.

GOVERNMENT OF INDIA  
MINISTRY OF ELECTRONICS AND INFORMATION TECHNOLOGY  
**RAJYA SABHA**  
**UNSTARRED QUESTION NO. 711**  
TO BE ANSWERED ON: 25.07.2025

**PROGRESS OF RRP ELECTRONICS SEMICONDUCTOR PLANT AT MIDC TALOJA**

**711.# SHRI DHAIRYASHIL MOHAN PATIL:**

Will the Minister of ELECTRONICS AND INFORMATION TECHNOLOGY be pleased to state:  
(a) the current status of the RRP Electronics semiconductor plant established at Maharashtra Industrial Development Corporation (MIDC) Taloja and the details of the actual progress so far;  
(b) whether the project has faced any delays or other difficulties; and  
(c) the timeline of the project and the time by which the plant will become fully functional?

**ANSWER**

MINISTER OF STATE FOR ELECTRONICS AND INFORMATION TECHNOLOGY  
(SHRI JITIN PRASADA)

(a) to (c): Government introduced Semicon India Programme with an outlay of Rs 76,000 crore, which provides:

- i. Fiscal support of 50% of the project cost on pari-passu basis for setting up of Silicon Complementary Metal- Oxide-Semiconductor (CMOS) based Semiconductor Fabs in India.
- ii. Fiscal support of 50% of Project Cost on pari-passu basis for setting up of Display Fabs in India.
- iii. Fiscal support of 50% of the Capital Expenditure on pari-passu basis for setting up of Compound Semiconductors / Silicon Photonics (SiPh) / Sensors (including Micro-Electro-Mechanical Systems) Fab/ Discrete Semiconductor Fab and Semiconductor Assembly, Testing, Marking and Packaging (ATMP) / Outsourced Semiconductor Assembly and Test (OSAT) facilities in India.
- iv. Product Design Linked Incentive of up to 50% of the eligible expenditure subject to a ceiling of ₹15 Crore per application and also “Deployment Linked Incentive” of 6% to 4% of net sales turnover over 5 years subject to a ceiling of ₹30 Crore per application for incentivising chip design

Application submitted by RRP S4E Innovation Pvt. Ltd. has for the establishment of Semiconductor OSAT unit through the project company RRP Electronics Limited has been received. India Semiconductor Mission duly examines every application on technical and financial parameters.

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