GOVERNMENT OF INDIA
MINISTRY OF ELECTRONICS AND INFORMATION TECHNOLOGY
RAJYA SABHA
UNSTARRED QUESTION NO. 3251
TO BE ANSWERED ON: 28.03.2025

PROGRESS OF SEMICON INDIA PROGRAMME

3251. SHRI KARTIKEYA SHARMA:

Will the Minister of ELECTRONICS AND INFORMATION TECHNOLOGY be pleased to state:

(a) the current progress of the Semicon India programme, including the number ofproposed/approved and operational plants and jobs created as of 2025, the details thereof, State-wise;
(b) whether Government has assessed the challenges faced by domestic firms in competing with global players like Taiwan and South Korea amidst supply chain disruptions, if so, the details thereof; and
(c) the specific measures taken or proposed, to train youth in semiconductor design and manufacturing, the details thereof?

ANSWER

MINISTER OF STATE FOR ELECTRONICS AND INFORMATION TECHNOLOGY
(SHRI JITIN PRASADA)

(a) to (c):Governmentintroduced Semicon India Programme with an outlay of Rs 76,000 crore, which provides:

i. Fiscal support of 50% of the project cost on *pari-passu* basis for setting up of Silicon Complementary Metal-Oxide-Semiconductor (CMOS) based Semiconductor Fabs in India.
ii. Fiscal support of 50% of Project Cost on *pari-passu* basis for setting up of Display Fabs in India.
iii. Fiscal support of 50% of the Capital Expenditure on *pari-passu* basis for setting up of Compound Semiconductors / Silicon Photonics (SiPh) / Sensors (including Micro-Electro-Mechanical Systems) Fab/ Discrete Semiconductor Fab and Semiconductor Assembly, Testing, Marking and Packaging (ATMP) / Outsourced Semiconductor Assembly and Test (OSAT) facilities in India.
iv. Product Design Linked Incentive of up to 50% of the eligible expenditure subject to a ceiling of ₹15 Crore per application and also "Deployment Linked Incentive" of 6% to 4% of net sales turnover over 5 years subject to a ceiling of ₹30 Crore per application for incentivising chip design.

Government has approved five (5) semiconductor manufacturing projects under Semicon India Program with cumulative investment of around Rs. 1,52,000 crore. This includes four projects in the state of Gujarat and one in the state of Assam. These projects are expected to generate more than 26,000 direct jobs depending on the maturity of factory automation. Moreover, Semiconductors are a foundational industry, which has significant potential for job creation in other sectors as well.

Government has also approved modernisation of Semi-Conductor Laboratory, Mohali to enhance efficiency and cycle time.

Further, to strengthen semiconductor manufacturing and creating a semiconductor ecosystem in the country, Government has entered in Memorandum of Understanding (MoU) with USA, European Union, Japan and Singapore.

Semiconductor manufacturing is very complex and Technology intensive sector which requires specialized skilled manpower. To address this, the following measures have been taken by the Government:

1. All India Council for Technical Education (AICTE) has launched the new curriculum for B. Tech in Electronics Engineering (Very Large-Scale Integration (VLSI) Design and Technology), Diploma in Integrated Circuit (IC) manufacturing and Minor Degree in Electronics Engineering (VLSI Design and Technology), as a step towards creation of Talent pool in Semiconductor domain.
2. Government has launched the Chips to Startup ('C2S') programme which plans to train 85,000 industry ready workforce at about 113 participating institutions in VLSI and Embedded System Design. More than 43,000 engineering students have been onboarded for training at 113 organizations under C2S Programme till date.
3. A Skilled Manpower Advanced Research and Training (SMART) Lab has been setup in NIELIT Calicut in 2022 with an aim to train one lakh engineers nation-wide within 5 years in VLSI and Embedded System design. More than 42,000 engineers have been trained nationwide using the SMART Lab.
4. Further, the following collaborations/ partnerships have been entered into by India Semiconductor Mission (ISM) to encourage skill development:

    (i) MoU between ISM with IISc and Lam Research: To train about 60,000 Indian engineers in the upcoming 10 years through Lam Research's Semiverse platform.

    (ii) MoU between ISM and IBM: To facilitate Indian students/professionals to build a broad skill base by gaining access to laboratories and research focal centers and establishing internship and fellowship programs.

    (iii) MoU between ISM with Purdue University: To promote the cutting-edge research and development and commercialization thereof, curating skilled talent pool and investment opportunities in India enabling the Indian professionals to explore their potential in the semiconductor and display space.

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